

Document Summary: Printronix 6000 C3 logic setup instructions including pcba layout for both pcba styles and control panel navigation

Document Type: Tech notes

Document Title:

Product Category: PTX

Model: 6040, 6240, 6080, 6280

Additional Keywords:

Links:

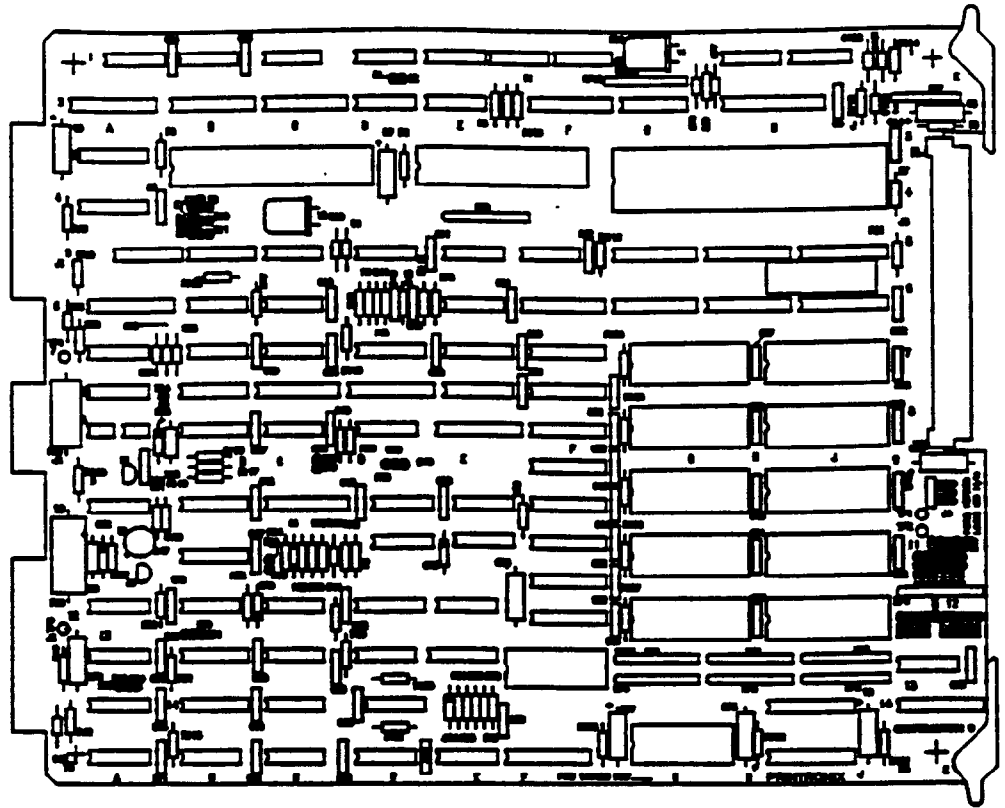
P-6000 LOGIC C PCB SETUP

The 6000 Series Printer uses a very sophisticated multi function Microprocessor based controller. The configuration of this PCBA can be very complex and involved. We have done our best to pre-configure your PCBA based on the information we were given at the time of your order. You may find that you need to modify the configuration as shipped!

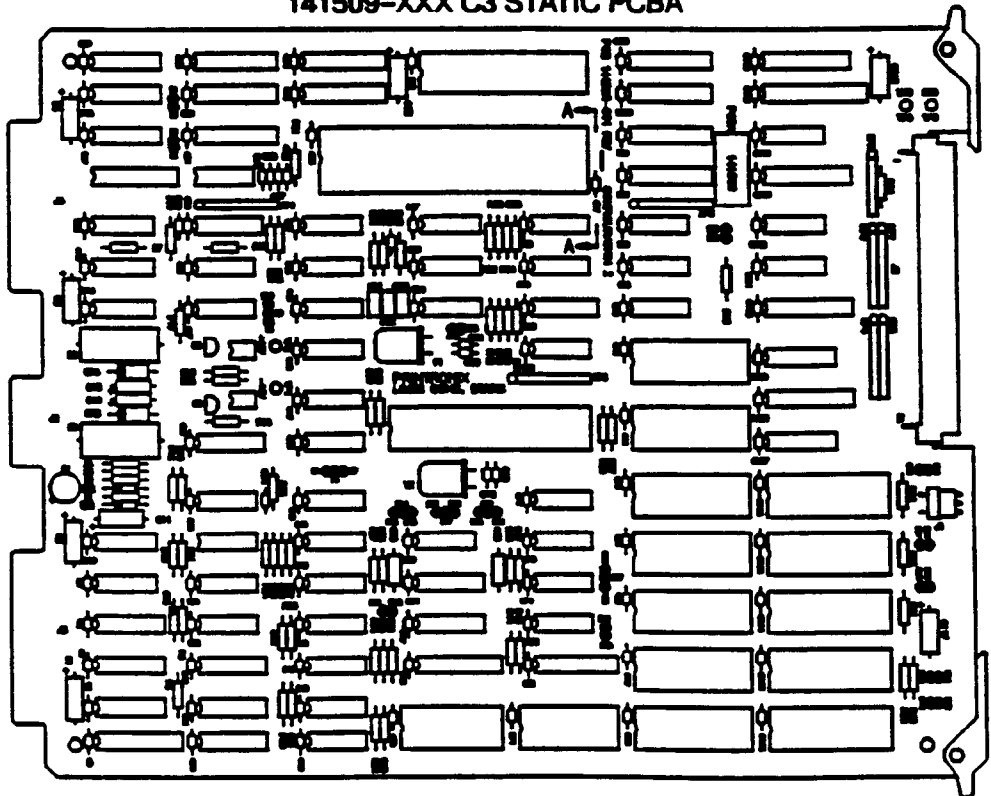
THIS APPLICATION NOTE HAS ALL OF THE INFORMATION YOU NEED TO CHECK AND MODIFY THE CONFIGURATION OF ANY REVISION OF THE LOGIC C PCBA.

Please read it carefully before installing your new board, and keep it as a future reference guide.

C3 H/S DRAM
107952-XXX C3 PCBA



C3 H/S STATIC
141509-XXX C3 STATIC PCBA



HAMMER RESET TIMER PCB

ON THE "DRAM" STYLE PCBA, IF BEING USED IN A 6X80 PRINTER, YOU MAY REQUIRE A "HAMMER RESET TIMER" PCBA = P/N 104363-001 AT SOCKET LOCATION "4A" IF YOU HAVE AN IC (CHIP) AT LOCATION "4A" THEN IT FUNCTIONALLY REPLACES THE "HRT" PCBA. IF YOU HAVE AN EMPTY SOCKET AT "4A" AND YOU ARE CONFIGURING FOR A 6X80 PRINTER, YOU WILL NEED TO ADD THE "HRT" PCBA. THE "STATIC" STYLE PCBA DOES NOT USE THE "HRT" PCBA.

Note differences in PCBA layout.

FIRMWARE REVISION	PART NUMBER	PROM LOCATIONS	
		C3 H/S DRAM	C/H/S STATIC
1.02A FONT	108948-001	11G	U70
1.02A FONT	108948-002	11J	U84
1.04E PROGRAM	148589-001	7G	U67
1.04E PROGRAM	148589-002	7J	U81
1.04E PROGRAM	148589-003	8G	U68
1.04E PROGRAM	148589-004	8J	U82

All level 1 proms listed above are 27128K - 150ns

Level 1 "NVRAM" (Low Profile) Denoted by markings "2804" P/N 107968-001 15G U57

LEVEL 2 C3 FIRMWARE VERSION 2.01 - 2-06A (Example below is of 2.06A)

FIRMWARE REVISION	PART NUMBER	PROM LOCATIONS	
		C3 H/S DRAM	C/H/S STATIC
2.04A FONT	148655-001	11G	U70
2.04A FONT	148655-002	11J	U84
2.06A PROGRAM	148679-001	7G	U67
2.06A PROGRAM	148679-002	7J	U81

All level 2 proms listed above are 27256K - 150ns

Level 2 "NVRAM" (Tall Profile) Denoted by markings "48Z02" or "DS1220" 15G U57 and is P/N 109795-001

LEVEL 2 C3 FIRMWARE VERSION 2.50E

FIRMWARE REVISION	PART NUMBER	PROM LOCATIONS	
		C3 H/S DRAM	C/H/S STATIC
2.04A FONT	148655-001	11G	U70
2.04A FONT	148655-002	11J	U84
2.50E PROGRAM	141572-001	7G	U67
2.50E PROGRAM	141572-002	8G	U68
2.50E PROGRAM	141572-003	7J	U81
2.50E PROGRAM	141572-004	8J	U82

All level 2 proms listed above are 27256K - 150ns

C3 H/S DRAM C3 H/S STATIC P6X80 P6X40 DESCRIPTION

TYPICAL REQUIRED JUMPER SETTINGS FOR P6XX0 PRINTERS

C3 H/S DRAM	C3 H/S STATIC	P6X80	P6X40	DESCRIPTION
E18-E19	E29-E30	IN	OUT	P6XX0 Select
E20-E21	E11-E12	IN	OUT	P6X80 NRESYNC Timer
E32-E33	E3-E4	IN	IN	P6X80 Phase Fire Enable (2.50 E F/W requires this jumper to be installed for both the 6x40 & 6x80 printers)
E34-E35	E31-E32	.	.	* 27256 EPROM Address (Program-0) C3 H/S DRAM = 7G & 7J C3 H/S STATIC = U67 & U81
E36-E37	E22-E23	IN	IN	CPU Clock
E38-E39	E26-E27	OUT	IN	P6X40 Hammer Load
E39-E40	E27-E28	IN	OUT	P6X Hammer Load
E41-E42	E33-E34	.	.	* 27256 EPROM Address (Program-1) C3 H/S DRAM = 8G - 8J C3 H/S STATIC = U68 & U82
E43-E44	E35-E36	.	.	* 27256 EPROM Address (Program-2) C3 H/S DRAM = 10G & 10J C3 H/S STATIC = U69 & U83
E45-E46	E37-E38	.	.	* 27256 EPROM Address (Font-0) C3 H/S DRAM = 11G & 11J C3 H/S STATIC = U70 & U84
E47-E48	E39-E40	.	.	* 27256 EPROM Address (Font-1) C3 H/S DRAM = 12G & 12J C3 H/S STATIC = U71 & U85
E49-E50	E5-E6	IN	IN	Data Latch Leading Edge Select
E50-E51	E6-E7	OUT	OUT	Data Latch Trailing Edge Select
E52-E53	E8-E9	IN	IN	ACK Leading Edge Select
E53-E54	E9-E10	OUT	OUT	ACK Trailing Edge Select

JUMPER LOCATION		DESCRIPTION
C3 H/S DRAM	C3 H/S STATIC (NEW)	JUMPER DESCRIPTIONS
E3-E6	E16-E17	NTXCLK Input - Serial Interface Transmit Clock**
E4-E7	E13-E14	NRXCLK Input - Serial Interface Receive Clock**
E5-E8	E19-E20	NEXTCLK Input - Serial Interface External Clock**
E6-E9	E20-E21	Clock to TC Input - Serial Interface**
E7-E10	E14-E15	Clock to RC Input - Serial Interface**
E8-E11	E17-E18	Timer C Output to Clock - Serial Interface**
E14-E15	E1-E2	Dataproducts Strobe Polarity (In=Inverted)
E18-E19	E29-E30	P6x80 Select In=P6x80 / Out=P6x40
E20-E21	E11-E12	P6x80 NRESYNC Timer Must be IN for P6x80
E32-E33	E3-E4	P6x80 Phase Fire Enable Must be IN for P6x80 (Ver. 2.50 E F/W requires this jumper to be installed on both 6x40 and 6x80 printers.)
E34-E35	E31-E32	* 27256 EPROM Address (Program -0) C3 H/S DRAM = 7G & 7J C3 H/S STATIC = U67 & U81
E36-E37	E22-E23	CPU Clock Must be IN
E38-E39	E26-E27	P6x40 Hammer Load (3.33MHz) Must be IN for P6x40
E39-E40	E27-E28	P6x80 Hammer Load (1.00MHz) Must be IN for P6x80
E41-E42	E33-E34	* 27256 EPROM Address (Program -1) C3 H/S DRAM = 8G & 8J C3 H/S STATIC = U68 & U82
E43-44	E35-E36	* 27256 EPROM Address (Program -2) C3 H/S DRAM = 10G & 10J C3 H/S STATIC = U69 & U83
E45-E46	E37-E38	* 27256 EPROM Address (Font-0) C3 H/S DRAM = 11G & 11J C3 H/S STATIC = U70 & U84
E47-E48	E39-E40	* 27256 EPROM Address (Font-1) C3 H/S DRAM = 12G & 12J C3 H/S STATIC = U71 & U85
E49-E50	E5-E6	Data Latch Edge Select IN=Leading Edge
E50-E51	E6-E7	Data Latch Edge Select IN=Trailing Edge
E52-E53	E8-E9	ACK Edge Select IN=Leading Edge
E53-E54	E9-E10	ACK Edge Select IN=Trailing Edge
E1-E2	E24-25	EPROM Wait State Jumper

FIRMWARE VERSIONS 1.02 - 1.04E PROGRAM

C3 H/S DRAM REMOVED = E34-35, E41-42, E43-44, E45-46, E47-48
C3 H/S STATIC REMOVED = E31-32, E33-34, E35-36, E37-38, E39-40

FIRMWARE VERSIONS 2.01 - 2.06A PROGRAM

C3 H/S DRAM REMOVED = E41-42, E43-44, E47-48
 INSTALLED = E34-35, E45-46
C3 H/S STATIC REMOVED = E33-34, E35-36, E39-40
 INSTALLED = E31-32, E37-38

FIRMWARE VERSIONS 2.50E

C3 H/S DRAM REMOVED = E47-48
 INSTALLED = E45-46, E34-35, E41-42, E43-44, E32-33
C3 H/S STATIC REMOVED = E39-40
 INSTALLED = E31-32, E33-34, E37-38, E35-36, E3-4

Besides configuring printer for serial/parallel thru setup menu, there is also a hardware jumper (on Logic C) that will physically enable serial or parallel operation! The jumper is a jumper package, that has 10 silver jumpers on its face, 5 of the jumpers have been cut (open) and the other 5 are shorted (closed) Location and orientation as follows!

On either style pcb, - orientation is as follows:

Serial = 5 cuts closest to gold edge connector of pcb.

Parallel = 5 jumpers closest to the gold edge connector of pcb.

Locations:

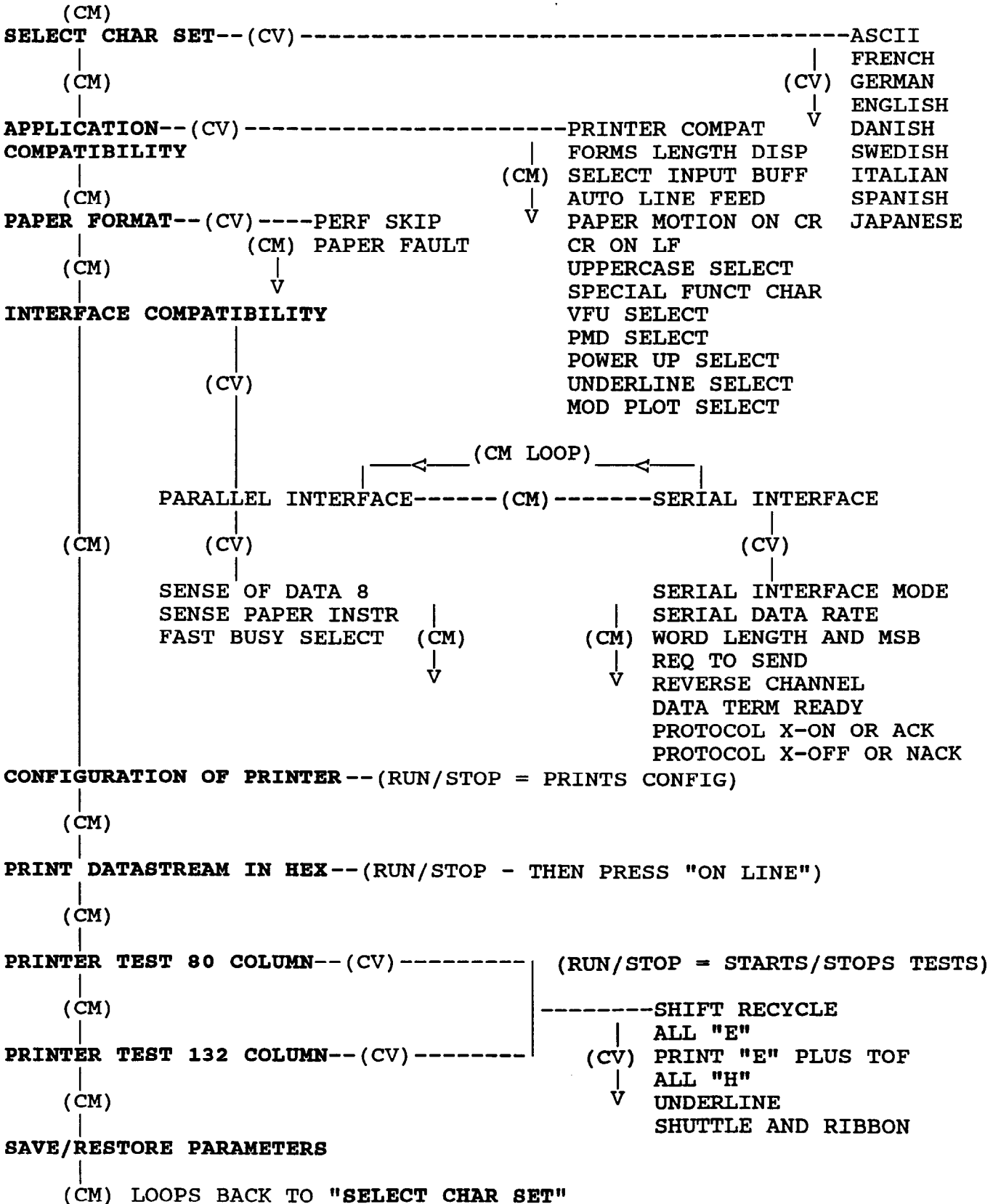
C3 H/S DRAM = Location 6A

C3 H/S STATIC = Location U10

Level 1 Setup Parameters

LEGEND:

CONFIG MENU-- |----- PRESS SIMULTANEOUSLY TO (CM) = CONFIG MENU
 RUN/STOP TEST-- |----- UNLOCK AND ENTER MENU! (CV) = CONFIG VALUE



Level 2 Setup Parameters

CONFIG MENU--
 RUN/STOP TEST--

PRESS SIMULTANEOUSLY TO
 UNLOCK AND ENTER MENU!

LEGEND:
 (CM) = CONFIG MENU
 (CV) = CONFIG VALUE

